

	Hits	Search Text	DBs	Time Stamp
1	1	5614847.pn.	USPAT	2003/12/08 11:11
2	52	schoellkopf.in.	EPO	2003/12/04 16:08
3	14	schoellkopf.in. with jean	EPO	2003/12/04 16:12
4	31585 0	nippon.as.	JPO	2003/12/04 16:13
5	68422	nippon.as. with telegr	JPO	2003/12/04 16:13
6	68422	nippon.as. with telegr with teleph	JPO	2003/12/04 16:13
7	0	nippon.as. with telegr with teleph and "212217"	JPO	2003/12/04 16:13
8	0	"212217"	JPO	2003/12/04 16:14
9	0	"JP 07 212217 A"	JPO	2003/12/04 16:15
10	68422	"nippon telegr & teleph".as.	JPO	2003/12/04 16:15
11	68422	"nippon telegr & teleph corp".as.	JPO	2003/12/04 16:15
12	0	"nippon telegr & teleph corp".as. and "212217"	JPO	2003/12/04 16:16
13	2	("6549037" "6388315").pn.	USPAT	2003/12/08 11:11
14	1	"6310509".PN.	USPAT	2003/12/08 11:17
15	1	"6246265".PN.	USPAT	2003/12/08 12:55
16	1	"5898321".PN.	USPAT	2003/12/08 11:36
17	1	"5568069".PN.	USPAT	2003/12/08 12:18
18	1	"5486785".PN.	USPAT	2003/12/08 12:19
19	1	"5391938".PN.	USPAT	2003/12/08 12:21
20	1	"5355035".PN.	USPAT	2003/12/08 12:22
21	1	6313695.pn.	USPAT	2003/12/08 13:26
22	232	circuit with (decouple ((turn switch) adj1 off)) with power with mode	USPAT	2003/12/08 13:37
23	18	circuit with (decouple ((turn switch) adj1 off)) with power with (operat\$5 adj1 mode)	USPAT	2003/12/08 13:35

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24	10	circuit with (decouple ((turn switch adj1 off)) with power with (operat\$5 adj1 mode)	US-PGPUB; EPO; JPO; IBM_TDB	2003/12/08 13:35
25	67	circuit with decoupl\$3 with power with mode	USPAT	2003/12/08 14:08
26	6	circuit with decoupl\$3 with power with mode	EPO; JPO; IBM_TDB	2003/12/08 14:04
27	50	((decoupl\$3 ((switch\$3 turn\$3 cut\$4) adj1 off)) near2 power) with (circuit with ((stand\$1by sleep idle) adj1 mode))	USPAT	2003/12/08 15:22
28	13	((decoupl\$3 ((switch\$3 turn\$3 cut\$4) adj1 off)) near2 power) with (circuit with ((stand\$1by sleep idle) adj1 mode))	EPO; JPO; IBM_TDB	2003/12/08 14:59
29	1	((decoupl\$3 ((switch\$3 turn\$3 cut\$4) adj1 off)) near2 power) with (circuit with (low\$1power adj1 mode))	USPAT	2003/12/08 15:24
30	2662	(power adj1 consumption) with (integrated adj1 circuit)	USPAT	2003/12/08 17:35
31	4	(power adj1 consumption) with (integrated adj1 circuit) with (logic adj1 circuitry)	USPAT	2003/12/08 15:30
32	0	(power adj1 consumption) with (integrated adj1 circuit) with (removed with circuitry)	USPAT	2003/12/08 15:31
33	0	(power adj1 consumption) with (integrated adj1 circuit) with removed with circuitry	USPAT	2003/12/08 15:34
34	146	(power adj1 consumption) with (integrated adj1 circuit) with circuitry	USPAT	2003/12/08 17:35
35	8	(power adj1 consumption) with (integrated adj1 circuit) with circuitry with mode	USPAT	2003/12/08 16:22
36	457	(power adj1 consumption) with (integrated adj1 circuit) and (leakage with current)	USPAT	2003/12/08 17:34
37	2	(decoupl\$3 near2 power) with (logic adj1 circuit)	USPAT	2003/12/08 16:22
38	386	(decoupl\$3 near2 power) with circuit	USPAT	2003/12/08 17:34
39	62	(decoupl\$3 near2 power) with circuit with (integrated adj1 circuit)	USPAT	2003/12/08 17:34
40	0	(decoupl\$3 near2 power) with circuit with (integrated adj1 circuit) with (low\$1power stand\$1by sleep idle)	USPAT	2003/12/08 15:44
41	2	(decoupl\$3 near2 power) with circuit with (integrated adj1 circuit) with (operation mode)	USPAT	2003/12/08 16:23

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42	1295	(processing adj1 unit) with power with circuit	USPAT	2003/12/08 16:00
43	3	(processing adj1 unit) with ((decoupl\$3 remov\$3) near2 power) with circuit	USPAT	2003/12/08 16:23
44	64	(first adj1 circuit) with (second adj1 circuit) with power with mode	USPAT	2003/12/08 16:08
45	31	(first adj1 circuit) with (second adj1 circuit) with power with mode with (de\$1coupl\$3 remov\$3 cut\$4 switch\$3 stop\$3 turn\$3)	USPAT	2003/12/08 16:32
46	0	6208171.uref.	USPAT	2003/12/08 16:19
47	2	(power adj1 consumption) with (integrated adj1 circuit) with circuitry with mode	US_PGPUB; EPO; JPO; IBM_TDB	2003/12/08 16:22
48	1	(decoupl\$3 near2 power) with (logic adj1 circuit)	US_PGPUB; EPO; JPO; IBM_TDB	2003/12/08 16:22
49	2	(decoupl\$3 near2 power) with circuit with (integrated adj1 circuit) with (operation mode)	US_PGPUB; EPO; JPO; IBM_TDB	2003/12/08 16:23
50	1	(processing adj1 unit) with ((decoupl\$3 remov\$3) near2 power) with circuit	US_PGPUB; EPO; JPO; IBM_TDB	2003/12/08 16:23
51	9	(first adj1 circuit) with (second adj1 circuit) with power with mode with (de\$1coupl\$3 remov\$3 cut\$4 switch\$3 stop\$3 turn\$3)	US_PGPUB; EPO; JPO; IBM_TDB	2003/12/08 16:24
52	201	(reduc\$3 near2 leakage) with ((integrated adj1 circuit) IC)	USPAT	2003/12/08 17:34
53	5	(reduc\$3 near2 (leakage near1 current)) with ((integrated adj1 circuit) IC) and (de\$1coupl\$3 near3 power)	USPAT	2003/12/08 16:35
54	155	(reduc\$3 near2 (leakage near1 current)) with ((integrated adj1 circuit) IC)	USPAT	2003/12/08 17:18
55	31	(reduc\$3 near2 (leakage near1 current)) with ((integrated adj1 circuit) IC)	EPO; JPO; IBM_TDB	2003/12/08 17:22
56	12	(arimoto.in. with kazutami) and mitsubishi.as. and (current adj1 consumption)	JPO	2003/12/08 17:26
57	1	(power adj1 consumption) with (integrated adj1 circuit)	IBM_TDB	2003/12/08 17:35
58	1	(decoupl\$3 near3 power) with circuit	IBM_TDB	2003/12/09 15:13
59	36	decoupl\$3 near3 power	IBM_TDB	2003/12/09 15:13

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60	663	transistor with ((gate adj1 dielectric) adj1 layer)	USPAT	2003/12/10 10:51
61	9	transistor with ((gate adj1 dielectric) adj1 layer) with thinner	USPAT	2003/12/10 12:28
62	214	(pass adj1 transistor) with parallel	USPAT	2003/12/10 12:30
63	214	(pass adj1 transistor) with parallel with transistor	USPAT	2003/12/10 12:31
64	0	(pass adj1 transistor) with parallel with (coupling adj1 transistor)	USPAT	2003/12/10 12:30
65	96	(pass adj1 transistor) near2 parallel with transistor	USPAT	2003/12/10 12:32
66	96	((pass adj1 transistor) near2 parallel) near3 transistor	USPAT	2003/12/10 13:20
67	0	((pass adj1 transistor) near2 parallel) near3 transistor) with opposite	USPAT	2003/12/10 12:34
68	0	((pass adj1 transistor) near2 parallel) near3 transistor) with polarity	USPAT	2003/12/10 12:34
69	5	((pass adj1 transistor) near2 parallel) near3 transistor same (integrated adj1 circuit)	USPAT	2003/12/10 13:15
70	53	((pass adj1 transistor) near2 parallel) near3 transistor same circuit	USPAT	2003/12/10 13:20
71	13	((pass adj1 transistor) near2 parallel) near3 transistor same circuit same power	USPAT	2003/12/10 13:20
72	0	((pass adj1 transistor) near2 parallel) near3 transistor same circuit same power	EPO; JPO; IBM_TDB	2003/12/10 13:20
73	0	((pass adj1 transistor) near2 parallel) near3 transistor same circuit	EPO; JPO; IBM_TDB	2003/12/10 13:20
74	2	((pass adj1 transistor) near2 parallel) near3 transistor	EPO; JPO; IBM_TDB	2003/12/10 13:20